Electronic Acknowledgement Receipt				
EFS ID:	3983105			
Application Number:	10632024			
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Title of Invention:	Synchronization of processor states			
First Named Inventor/Applicant Name:	Gerard Chauvel			
Customer Number:	23494			
Filer:	Utpal Dilipkumar Shah/Jennifer Ringer			
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)			
1		05418RFOADATEDJULY242008. pdf	88285 6al6al7a99leae999leb657a2a6699504	yes	6			

Multipart Description/PDF files	Multipart Description/PDF files in .zip description				
Document Description	Start	End			
Amendment After Final	1	1			
Claims	2	5			
Applicant Arguments/Remarks Made in an Amendment	6	6			
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Information

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.